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**APPLICANTS**

Yoshikazu Saitoh, Hamura, JAPAN;

Sadayuki Morita, Higashiyamato, JAPAN;

Takahiro Sonoda, Fussa, JAPAN;

**\*\* CONTINUING DATA***YES 12/15/04 TMT*

This application is a CON of 09/906,060 07/17/2001 PAT 6,711,075

**\*\* FOREIGN APPLICATIONS***YES 12/15/04 TMT*

JAPAN 2000-218234 07/19/2000

**IF REQUIRED, FOREIGN FILING LICENSE GRANTED****\*\* 06/12/2004**

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR  COUNTRY	SHEETS  DRAWING	TOTAL  CLAIMS	INDEPENDENT  CLAIMS
Verified and Acknowledged <i>YES</i>	Examiner's Signature <i>[Signature]</i> Initials <i>TMT</i>	JAPAN	20	19	4

**ADDRESS**

020457

ANTONELLI, TERRY, STOUT &amp; KRAUS, LLP

1300 NORTH SEVENTEENTH STREET

SUITE 1800

ARLINGTON, VA

22209-9889

**TITLE**

Semiconductor wafer, semiconductor chip, and manufacturing method of semiconductor device

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